

Digital Electronics

①

Number representation

1. Binary
 2. herea octal number
 3. Decimal number
 4. herea decimal
- to convert Binary to octal make a grouping of 3 bits and for here make it with 4 bits.

Radix Complement: $\rightarrow$

(i) 9's Complement = Sub. the given no from 9's
(in number 9 should be in same order as that no.)

Ex in $(100)_{10}$ ie 3 bit it should be 999
for 4 bit = 9999
9's complement of 0987 = 9999
$$\begin{array}{r} 9999 \\ - 0987 \\ \hline 1012 \end{array} \rightarrow 9's \text{ Comp.}$$

ii)

10's Complement

Sub. the given no. as in 9's
Complement the only change is ones digit
(i.e. last digit $10 - 7 = 3$) must be sub. from 10.

or simply take 9's complement and then add 1 to the no. you got. Ex $98 = 02$

③ 1's complement

to find 1's complement just convert 1's into zeroes and 0's into ones.

Ex.

$$110101 \rightarrow 001010$$

(4) 2's complement $\rightarrow$

to find 2's complement, write all zeroes same upto last 1 (including 1 also) ^{from right side} and then convert 1 $\rightarrow$ 0. or and 1 to 1's complement

Ex $1101100 \Rightarrow 0010100$

Subtraction with complement

1. using 10's complement $\rightarrow$

- Step 1. take complement which is going to subtract
- Step 2. add it with other no.
- Step 3. discard carry if any.
- Step 4. if you do not get any carry then

5 put a -ve sign before the ^{10's} complement of ②
result you got.

Same for 9's complement also.

using 1's complement: $\rightarrow$

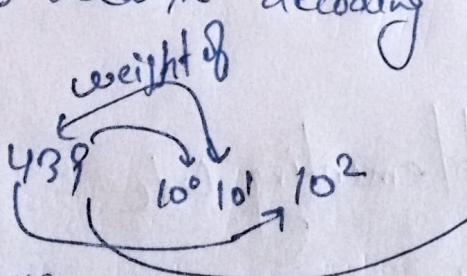
In 1's complement all steps are same the only change
is, add the carry to the result if you got
if no carry then same process you did
last time.

Signed binary number: $\rightarrow$

0 $\rightarrow$ for +ve no. $\rightarrow$ used at leftmost place
1 $\rightarrow$ for -ve no.

Binary Code $\rightarrow$

we use these codes because decoding becomes easy
than binary system.

1. Weighted Code $\rightarrow$ Ex 439  $10^2, 10^1, 10^0 \rightarrow 2^2, 2^1, 2^0$
2. Non weighted Code Some weight code 8421, 8421
3. Reflective Code 5421, 2421
4. Alphabetic Code 5411, 7421
5. Error detecting and correcting codes.

NOTE after 5 we give more high weightage from left side to write any no. in Binary system.

BCD (Binary Code decimal)

396 $\rightarrow$ 0011 1001 0110

Note after 9 we can not rep. a digit in BCD.

BCD addition

if $\text{Sum} > 9$ then add 0110

Ex $\begin{array}{r} 6 \\ + 7 \\ \hline \end{array} = \begin{array}{r} 0110 \\ 0111 \\ \hline 1101 \end{array} \rightarrow \textcircled{13} \times (\text{not valid})$

then. $\begin{array}{r} 1101 \\ 0110 \\ \hline 0011 \end{array}$

$\begin{array}{r} 0001 \\ \hline \textcircled{1} \end{array}$

$\textcircled{3}$

Self Complementing code

Excess-3 $\rightarrow$ unweighted code

add 0011 to each corresponding BCD

Ex $\begin{array}{r} 0001 \ 0011 \\ + 0011 \ 0011 \\ \hline 0100 \ 0100 \end{array} \rightarrow \text{in Excess 3 code}$

Gray Code / Cyclic Code / unit distance code

(5)

- unweight code
- not an arithmetic use

0 → 0000
1 → 0001
2 → 0011
3 → 0010
4 → 0110
5 → 0111

6 → 0101
7 → 0100
8 → 1100
9 → 1101
10 → 1111
11 → 1110

	00	01	11	10
00	0	1	2	3
01	7	6	5	4
11	8	9	10	11
10	15	14	13	12

12 → 1010
13 → 1011
14 → 1001
15 → 1000

Conversion of Binary to Gray Code

step 1. write as it is MSD

step 2 → then add MSD to next digit (bit) and next bit to next bit and so on record only sum discard the carry.

Ex

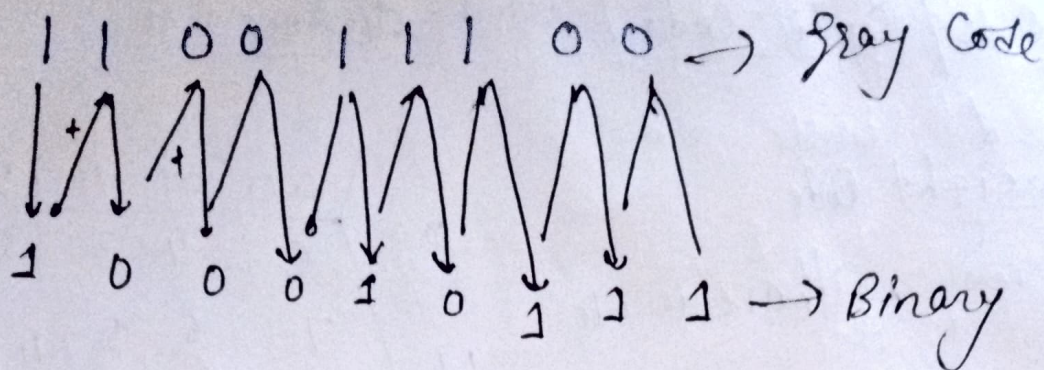
1 0 0 0 1 0 1 1 0 → Binary

1 1 0 0 1 1 1 0 1 → Gray Code

Gray Code to Binary →

write MSD then add each result with next digit (bit) of gray code (1st result is MSD).

Ex



Alphanumeric Codes: →

- ASCII
- total alphanumeric group > 36
- it include, ten decimal digit, 26 letter, and certain special character such as \$.
- it must be coded with a minimum 6 bits.

Ex

A = 1000001, B = 1000010

Error detecting Code →

- code should be with even or odd 1's.
- one extra bit helps to detect error.
- error can not be corrected.
- if even no. of error bits present in code error then it may not give error.

Application of Code

1. BCD used in digital clocks, digital thermometers, digital meters, and other devices with seven segment display.

Gray codes only one bit changes in next number ⑦
are used in shift position encoders.

• Ex-3 used in old computer and self complementary.

logic gates: $\rightarrow$

1. Basic logic gate: $\rightarrow$ AND, OR, NOT
2. universal gate $\rightarrow$ NAND, NOR
3. other gates $\rightarrow$ Buffer, XOR (Exclusive OR), Exclusive NOR

$$\begin{aligned} & \downarrow \\ & x\bar{y} + \bar{x}y \\ & x \oplus y \end{aligned}$$

$$\begin{aligned} & xy + \bar{x}\bar{y} \\ & x \odot y \end{aligned}$$

literal: $\rightarrow$

it is a variable or the complement of variable.

$\leftarrow$ Boolean Algebra: $\rightarrow$

1. Commutative law $A+B = B+A, AB = BA$
2. Associative law $A+(B+C) = (A+B)+C, A(BC) = (AB)C$
3. Distributive law $A(B+C) = AB+AC$

$$A+0 = A$$

$$1+A = 1$$

$$A \cdot 0 = 0$$

$$A+A = A$$

$$A+A = 1$$

$$A \cdot A = A$$

$$A \cdot \bar{A} = 0$$

$$\bar{\bar{A}} = A$$

$$A+AB = A$$

$$A+\bar{A}B = A+B$$

$$(A+B)(A+C) = A+BC$$

Demorgan's Law! $\rightarrow$

$$\overline{xy} = \overline{x} + \overline{y} \quad , \quad \overline{x+y} = \overline{x} \cdot \overline{y}$$

Min term! $\rightarrow$

write ~~A for 1~~ $1 = A$ Symbol of sop = Σ
 $0 = \overline{A}$

we find here sop (sum of product)

Ex

$$x\overline{y}z + xyz + \overline{x}\overline{y}z$$

NOTE we write sum of all those term in which we are getting 1 as output.

Max term! $\rightarrow$

write $1 = \overline{A}$ Symbol of pos $\Rightarrow \Pi$
 $0 = A$

Here we calculate pos (product of sum).

Ex $(x+y)(\overline{x}+y)$

Note $\rightarrow$ we write the product of sum of all those there in which we are getting 0 as output

NOTE sop and pos are complement to each other.

AB \ CD	00	01	11	10
	00	01	11	10
00				
01				
11				
10				

• for minterms make grouping of 1's.

• In grouping we can include only 2^n terms.

• try to include more terms.

• then we write SOP

* for Max terms grouping should include zeros.

and other thing are same.

* Here we will write POS.

• Don't Care Condition:->

There are some condition at which our output do not depend, there we can use 1 or 0 acc. our requirement to write logic and it will not affect the output.